

JFW



Attorney's Docket No. 42P15751
Confirmation No.: 2359

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Steven M. Bennett, et al.

Application No.: 10/663,205

Filed: September 15, 2003

For: VECTORING AN INTERRUPT OR
EXCEPTION UPON RESUMING
OPERATION OF A VIRTUAL
MACHINE

Examiner: Not yet assigned

Art Unit: 2121

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Mail Stop: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 23, 2004.

(Date of Deposit)

Dianne Neathery
(Typed or printed name of person mailing correspondence)

Dianne Neathery
(Signature of person mailing correspondence) November 23, 2004
(Date)

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

X 37 C.F.R. §1.97(b).

_____ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

_____ A statement pursuant to 37 C.F.R. §1.97(e) or

_____ A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

_____ 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:

- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
- (2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: November 23, 2004

Marina Portnova
Marina Portnova
Reg. No. 45,750

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025
(408) 720-8300

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

(Complete if Known)

Application Number	10/663,205
Filing Date	September 15, 2003
First Named Inventor	Bennett, Steven
Group Art Unit	2121
Examiner Name	Unknown

Sheet 1 of 6

Attorney Docket No: 42P15751

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US-20010027511	10/04/2001	Wakabayashi, Masaki , et al.	
	US-20020007456	01/17/2002	Peinado, Marcus , et al.	
	US-20020023032	02/21/2002	Pearson, Siani L., et al.	
	US-20020147916	10/10/2002	Strongin, Geoffrey S., et al.	
	US-20020166061	11/07/2002	Falik, Ohad , et al.	
	US-20020169717	11/14/2002	Challener, David C.	
	US-20030074548	04/17/2003	Cromer, Daryl C., et al.	
	US-20030115453	06/19/2003	Grawrock, David W.	
	US-20030126442	07/03/2003	Glew, Andrew F., et al.	
	US-20030126453	07/03/2003	Glew, Andrew F., et al.	
	US-20030159056	08/21/2003	Cromer, Daryl C., et al.	
	US-20030188179	10/02/2003	Challener, David C., et al.	
	US-20030196085	10/16/2003	Lampson, Butler W., et al.	
	US-20040117539	06/17/2004	Bennett, Steve , et al.	
	US-3,699,532	10/17/1972	Schaffer, Harry G., et al.	
	US-3,996,449	12/07/1976	Attanasio, Clement R., et al.	
	US-4,207,609	06/10/1980	Luiz, Fernando A., et al.	
	US-4,319,233	03/09/1982	Matsuoka, Michihiro, et al.	
	US-4,403,283	09/06/1983	Myntti, Jon N., et al.	
	US-4,419,724	12/06/1983	Branigin, Michael H., et al.	
	US-4,430,709	02/07/1984	Schleupen, Richard, et al.	
	US-4,621,318	11/04/1986	Maeda, A.	
	US-4,802,084	01/31/1989	Ikegaya, Hiroshi, et al.	
	US-4,975,836	12/04/1990	Hirosawa, Toshio, et al.	
	US-5,187,802	02/16/1993	Inoue, Taro , et al.	
	US-5,230,069	02/16/1993	Brelsford, David P., et al.	
	US-5,237,616	08/17/1993	Abraham, Dennis G., et al.	

EXAMINER**DATE CONSIDERED**

Substitute for form 1449A/PTO
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known

Application Number	10/663,205
Filing Date	September 15, 2003
First Named Inventor	Bennett, Steven
Group Art Unit	2121
Examiner Name	Unknown

Sheet 2 of 6

Attorney Docket No: 42P15751

US-5,287,363	02/15/1994	Wolf, Paul I., et al.
US-5,295,251	03/15/1994	Wakui, Fujio , et al.
US-5,361,375	11/01/1994	Ogi, Yoshifumi
US-5,469,557	11/21/1995	Salt, Tom , et al.
US-5,506,975	04/09/1996	Onodera, Osama
US-5,555,385	09/10/1996	Osisek, Damian L.
US-5,555,414	09/10/1996	Hough, Roger E., et al.
US-5,560,013	09/24/1996	Scalzi, Casper A., et al.
US-5,574,936	11/12/1996	Ryba, Edward G., et al.
US-5,604,805	02/18/1997	Brands, Stefanus A.
US-5,633,929	05/27/1997	Kaliski, Jr., Burton S.
US-5,668,971	11/16/1997	Neufeld, E. D.
US-5,684,948	11/04/1997	Johnson, James S., et al.
US-5,706,469	01/06/1998	Kobayashi, Souichi
US-5,740,178	04/14/1998	Jacks, Steven A., et al.
US-5,752,046	05/12/1998	Oprescu, Florin, et al.
US-5,809,546	09/15/1998	Greenstein, Paul G., et al.
US-5,825,880	10/20/1998	Sudia, Frank W., et al.
US-5,919,257	07/06/1999	Trostle, Jonathan
US-5,935,242	08/10/1999	Madany, Peter W., et al.
US-5,935,247	08/10/1999	Pai, Hsin-Ying, et al.
US-5,944,821	08/31/1999	Angelo, Michael F.
US-5,956,408	09/21/1999	Arnold, Todd W.
US-5,978,475	11/02/1999	Schneier, Bruce , et al.
US-6,035,374	03/07/2000	Panwar, Ramesh , et al.
US-6,044,478	03/28/2000	Green, Daniel W.
US-6,088,262	07/11/2000	Nasu, Hiroaki
US-6,093,213	07/25/2000	Favor, John G., et al.
US-6,108,644	08/22/2000	Goldschlag, David M., et al.
US-6,131,166	10/10/2000	Wong-Isley, Becky
US-6,175,924	01/16/2001	Arnold, Todd W.
US-6,188,257	02/13/2001	Buer, Mark L.
US-6,199,152	03/06/2001	Kelly, Edmund J., et al.
US-6,275,933	08/14/2001	Fine, Michael, et al.
US-6,282,650	08/28/2001	Davis, Derek L.
US-6,327,652	12/04/2001	England, Paul , et al.
US-6,397,379	05/28/2002	Yates, Jr., John S., et al.
US-6,529,909	03/04/2003	Bowman-Amuah, Michel K.
US-6,560,627	05/06/2003	McDonald, Michael F., et al.
US-6,609,199	08/19/2003	DeTreville, John
US-6,615,278	09/02/2003	Curtis, Bryce A.
US-6,651,171	11/18/2003	England, Paul, et al.
US-6,678,825	01/13/2004	Ellison, Carl M., et al.
US-6,684,326	01/27/2004	Cromer, Daryl C., et al.

EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	10/663,205
		Filing Date	September 15, 2003
		First Named Inventor	Bennett, Steven
		Group Art Unit	2121
		Examiner Name	Unknown
Sheet 3 of 6		Attorney Docket No: 42P15751	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
/	DE-DE4217444	12/03/1992	Toyohisa, Imada , et al.		
	EP-0602867	06/22/1994	Protheroe, Robert L., et al.		
	EP-EP0473913	03/11/1992	Farrell, Joel A.		
	EP-EP0600112	06/08/1994	Wimmer, Manfred D.		
	EP-EP0892521	01/20/1999	Zamek, Steven		
	EP-EP0930567	07/21/1999	Brewer, Jason M.		
	EP-EP0961193	12/01/1999	Laczko, Sr., Frank L.		
	EP-EP0965902	12/22/1999	Force, Gordon , et al.		
	EP-EP1030237	08/23/2000	Proudler, John , et al.		
	EP-EP1055989	11/29/2000	Proudler, Graeme J., et al.		
	EP-EP1056014	11/29/2000	Proudler, Graeme J., et al.		
	EP-EP1085396	03/21/2001	Proudler, Graeme J., et al.		
	EP-EP1146715	10/17/2001	Wang, Xin , et al.		
	EP-EP1209563	05/29/2002	England, Paul , et al.		
	EP-EP1271277	01/02/2003	Behar, Yaachov		
	JP-JP2000076139	03/14/2000	Tanno, Masaaki , et al.		
	WO-WO0021238	04/13/2000	Drews, Paul C.		
	WO-WO0062232	10/19/2000	Headrick, Samuel P., et al.		
	WO-WO0127723	04/19/2001	Vamvakas, Athanasia , et al.		
	WO-WO0127821	04/19/2001	Chen, Liqun , et al.		
	WO-WO0163994	08/30/2001	Van Sant, Glen , et al.		
	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.		
	WO-WO0175565	10/11/2001	Ellison, Carl M.		
	WO-WO0175595	10/11/2001	Ellison, Carl M., et al.		
	WO-WO0201794	01/03/2002	Ellison, Carl		
	WO-WO02060121	08/01/2002	Grawrock, David W.		
	WO-WO02086684	10/31/2002	Proudler, Graeme J.		
	WO-WO0217555	02/28/2002	Ford, Warwick		
	WO-WO03058412	07/17/2003	Glew, Andrew , et al.		
	WO-WO9524696	09/14/1995	Mooney, David M., et al.		
	WO-WO9729567	08/14/1997	Gressel, Carmi , et al.		
	WO-WO9812620	03/26/1998	Nishiuchi, Taiki , et al.		
	WO-WO9834365	08/06/1998	Devanbu, Premkumar T., et al.		
	WO-WO9844402	10/08/1998	Bramhill, Ian D., et al.		
	WO-WO9905600	02/04/1999	Garst, Blaine , et al.		
	WO-WO9909482	01/15/2002	Bausch, Jean		
	WO-WO9918511	04/15/1999	Edrich, David R.		

EXAMINER**DATE CONSIDERED**

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known

Application Number	10/663,205
Filing Date	September 15, 2003
First Named Inventor	Bennett, Steven
Group Art Unit	2121
Examiner Name	Unknown

Sheet 4 of 6

Attorney Docket No: 42P15751

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
?	WO-WO9957863	11/11/1999	Hayes Jr., Kent F., et al.		
?	WO-WO9965579	12/23/1999	Bond, Eugene T.		

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	✓	BERG, CLIFF , "How Do I Create a Signed Applet?", <u>Dr. Dobb's Journal</u> , (08/1997),1-9	
	✓	BRANDS, STEFAN , "Restrictive Blinding of Secret-Key Certificates", <u>SPRINGER-VERLAG XP002201306</u> , (1995),Chapter 3	
	✓	CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", <u>7th Annual IEEE Symposium, FCCM '99 Proceedings</u> , XP010359180, ISBN 0-7695-0375-6, Los Alamitos, CA, (4/21/1999), 209-221	
	✓	COMPAQ COMPUTER CORPORATION, "Trusted Computing Platform Alliance (TCPA) Main Specification Version 1.1a", XP002272822, (1/25/2001),1-321	
	✓	COULOURIS, GEORGE, et al., "Distributed Systems, Concepts and Designs", 2nd Edition, (1994), 422-424	
	✓	CRAWFORD, JOHN , "Architecture of the Intel 80386", <u>Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86)</u> , (October 6, 1986),155-160	
	✓	DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", <u>Proceedings of the Symposium on Security and Privacy, IEEE Comp. Soc. Press</u> , ISBN 0-8186-1939-2,(May 1989),	
	✓	FABRY, R.S., "Capability-Based Addressing", <u>Fabry, R.S., "Capability-Based Addressing," Communications of the ACM</u> , Vol. 17, No. 7, (July 1974),403-412	
	✓	FRIEDER, GIDEON, "The Architecture And Operational Characteristics of the VMX Host Machine", <u>The Architecture And Operational Characteristics of the VMX Host Machine, IEEE</u> , (1982),9-16	
	✓	GOLDBERG, ROBERT P., "Survey of Virtual Machine Research", <u>COMPUTER Magazine</u> , (06/1974), 34-35	
	✓	GONG, LI , et al., "Going Beyond the Sandbox: An Overview of the New Security Architecture in the Java Development Kit 1.2", <u>Proceedings of the USENIX Symposium on Internet Technologies and Systems</u> , Monterey, CA,(12/1997),	
	✓	GUM, P. H., "System/370 Extended Architecture: Facilities for Virtual Machines", <u>IBM J. Research Development</u> , Vol 27, Number 6, (11/1983), 530-544	
	✓	HEINRICH, JOE, "MIPS R4000 Microprocessor User's Manual, Second Edition", <u>Chapter 4 "Memory Management"</u> , (6/11/1993), 61-97	
	✓	HP MOBILE SECURITY OVERVIEW, "HP Mobile Security Overview", (Sept. 2002), 1-10	

EXAMINER**DATE CONSIDERED**

Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/663,205
		Filing Date	September 15, 2003
		First Named Inventor	Bennett, Steven
		Group Art Unit	2121
		Examiner Name	Unknown
Sheet 5 of 6		Attorney Docket No: 42P15751	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		IBM, "Information Display Technique for a Terminate Stay Resident Program IBM Technical Disclosure Bulletin", <u>TDB-ACC-No. NA9112156</u> , Vol. 34, Issue 7A, (12/1/1991), 156-158	
		IBM CORPORATION, "IBM ThinkPad T30 Notebooks", <u>IBM Product Specification, located at www-1.ibm.com/services/files/cisco t30 spec sheet 070202.pdf</u> , last visited June 23, 2004, (July 2, 2002), 1-6	
		INTEL, "IA-32 Intel Architecture Software Developer's Manual", <u>Volume 3: System Programming Guide</u> , Intel Corporation - 2003, 13-1 through 13-24	
		INTEL, "Intel386 DX Microprocessor 32-Bit CMOS Microprocessor With Integrated Memory Management", (1995), 5-56	
		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", <u>Intel Product Specification, Order Number 245359-001</u> , (01/2000), 1-112	
		INTEL CORPORATION, "Intel 82802AB/82802AC Firmware Hub (FWH)", <u>Intel Product Datasheet, Document Number 290658-004</u> , (November 2000), 1-6, 17-28	
		INTEL CORPORATION, "Intel IA-64 Architecture Software Developer's Manual", <u>Volume 2: IA-64 System Architecture, Order Number 245318-001</u> , (01/2000), i, ii, 5.1-5.3, 11.1-11.8, 11.23-11.26	
		KARGER, PAUL A., et al., "A VMM Security Kernel for the VAX Architecture", <u>Proceedings of the Symposium on Research in Security and Privacy, XP010020182</u> , ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990), 2-19	
		KASHIWAGI, KAZUHIKO, et al., "Design and Implementation of Dynamically Reconstructing System Software", <u>Software Engineering Conference, Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996</u> , Los Alamitos, CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8, (1996),	
		LAWTON, KEVIN, et al., "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques", <u>http://www.plex86.org/research/paper.txt</u> , (11/29/1999), 1-31	
		LUKE, JAHN, et al., "Replacement Strategy for Aging Avionics Computers", <u>IEEE AES Systems Magazine, XP002190614</u> , (March 1999),	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press LLC, USA XP002201307</u> , (1997), 475	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press Series on Discrete Mathematics and its Applications, Boca Raton, FL, XP002165287</u> , ISBN 0849385237, (Oct. 1996), 403-405, 506-515, 570	
		MOTOROLA, "M68040 User's Manual", (1993), 1-1 to 8-32	
		NANBA, S., et al., "VM/4: ACOS-4 Virtual Machine Architecture", <u>VM/4: ACOS-4 Virtual Machine Architecture, IEEE, (1985), 171-178</u>	
		RICHT, STEFAN, et al., "In-Circuit-Emulator Wird Echtzeittauglich", <u>Elektronik, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620</u> , (100-103), 8-6-1991	

EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		<i>Complete if Known</i>	
		Application Number	10/663,205
		Filing Date	September 15, 2003
		First Named Inventor	Bennett, Steven
		Group Art Unit	2121
		Examiner Name	Unknown
Sheet 6 of 6		Attorney Docket No: 42P15751	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", <u>Proceedings of the 9th USENIX Security Symposium</u> , XP002247347, Denver, Colorado, (8/14/00), 1-17	
		ROSENBLUM, M., "Virtual Platform: A Virtual Machine Monitor for Commodity PC", <u>Proceedings of the 11th Hotchips Conference</u> , (8/17/1999), 185-196	
		RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/node.asp?id=1158 , 1-2	
		RSA SECURITY, "RSA SecurID Authenticators", www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf , 1-2	
		RSA SECURITY, "Software Authenticators", www.srasecurity.com/node.asp?id=1313 , 1-2	
		SAEZ, SERGIO , et al., "A Hardware Scheduler for Complex Real-Time Systems", <u>Proceedings of the IEEE International Symposium on Industrial Electronics</u> , XP002190615, (July 1999), 43-48	
		SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", <u>Wiley, John & Sons, Inc.</u> , XP002939871; ISBN 0471117099, (Oct. 1995), 47-52	
		SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithm, and Source Code in C", <u>Wiley, John & Sons, Inc.</u> , XP002138607; ISBN 0471117099, (Oct. 1995), 56-65	
		SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code C", <u>Wiley, John & Sons, Inc.</u> , XP0021111449; ISBN 0471117099, (Oct. 1995), 169-187	
		SCHNEIER, BRUCE, "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; <u>Wiley, John & Sons, Inc.</u> , XP002251738; ISBN 0471128457, (Nov. 1995), 28-33; 176-177; 216-217; 461-473; 518-522	
		SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", <u>Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA</u> , (Nov. 2001),	

EXAMINER

DATE CONSIDERED